

Electronic Acknowledgement Receipt

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International Application Number:	
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Title of Invention:	System and method for optimizing interconnections of components in a multichip memory module
First Named Inventor/Applicant Name:	Jeffery W. Janzen
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Information Disclosure Statement (IDS) Filed (SB/08)	SIDS_03062009_2002-1152_00 US.pdf	99969 69591347f5d412dc35fb0f90f60e3355adb21e5f	no	3

Warnings:

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National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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